

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2823 Examiner: To be assigned

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Applicant(s):	Junichi HIKITA et al.	
Serial No. :	10/626,524 )	)
Filed :	July 25, 2003	) ) CTATUS DEQUEST
For :	Method of producing a semiconductor Device by dividing a semiconductor wafer Into separate pieces of semiconductor chips (as amended)	) <u>STATUS REQUEST</u> ) . ) . ) )
Attorney Ref:	AI 290 D1	)
		January 10, 2006

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. If the response to this status request is made by telephone, please ask for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

January 10, 2006

Date

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